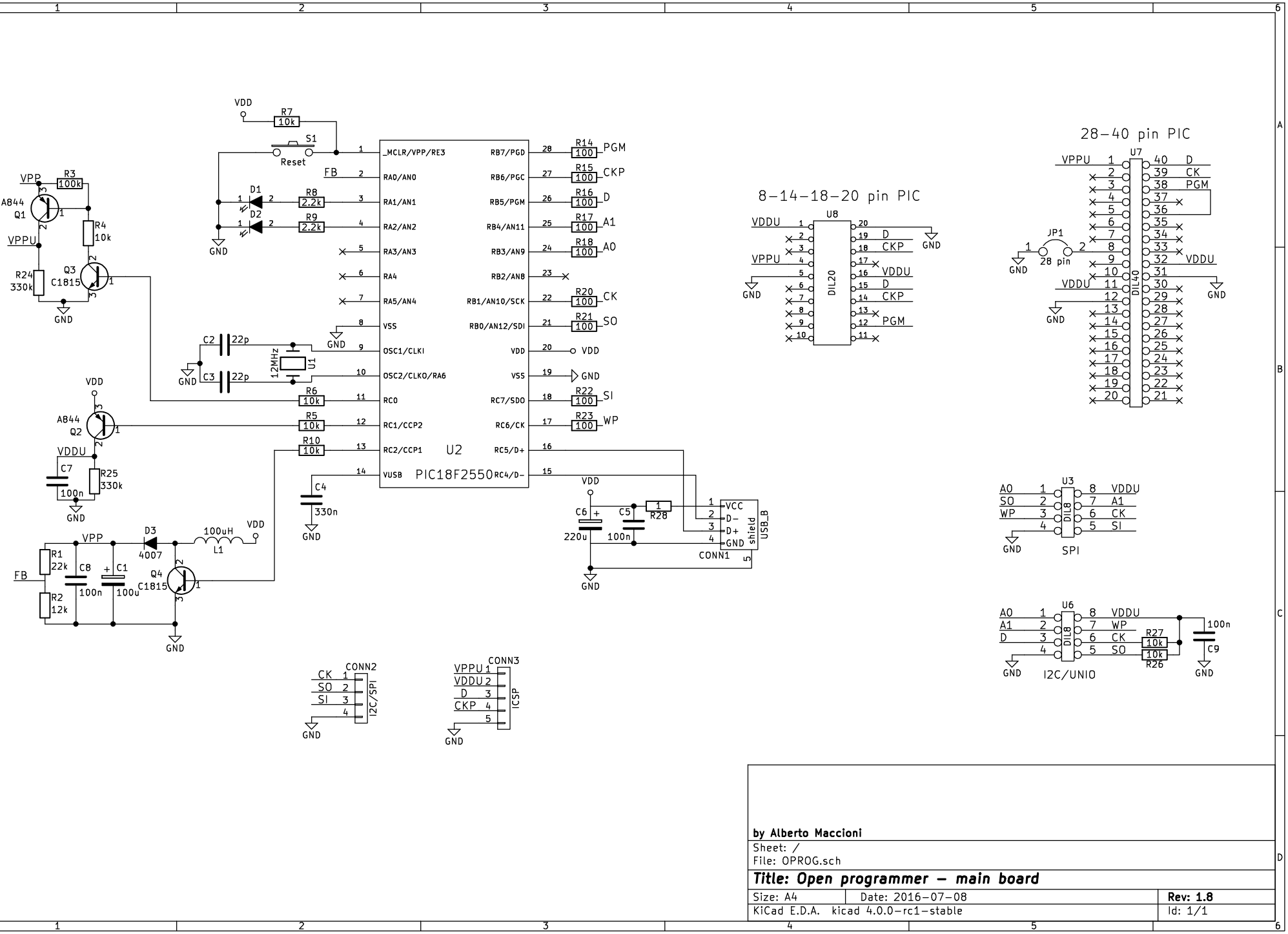

VDD
R7
10k
S1
Reset
FB
D1
D2
R8
2.2k
R9
2.2k
GND
VPP
R3
100k
AB44
Q1
R4
10k
VPPU
R24
330k
Q3
C1815
GND
C2
22p
C3
22p
12MHz
U1
VDD
AB44
Q2
VDDU
C7
100n
R25
330k
GND
R6
10k
R5
10k
R10
10k
C4
330n
GND
U2
PIC18F2550
_MCLR/VPP/RE3
RA0/AN0
RA1/AN1
RA2/AN2
RA3/AN3
RA4
RA5/AN4
VSS
OSC1/CLKI
OSC2/CLKO/RA6
RC0
RC1/CCP2
RC2/CCP1
VUSB
RB7/PGD
RB6/PGC
RB5/PGM
RB4/AN11
RB3/AN9
RB2/AN8
RB1/AN10/SCK
RB0/AN12/SDI
VDD
VSS
RC7/SDO
RC6/CK
RC5/D+
RC4/D-
R14
100
PGM
R15
100
CKP
R16
100
D
R17
100
A1
R18
100
A0
R20
100
CK
R21
100
SO
R22
100
SI
R23
100
WP
VDD
C6
220u
C5
100n
R28
1
VCC
D-
D+
GND
shield
USB_B
CONN1
GND
VPP
D3
4007
100uH
L1
VDD
R1
22k
C8
100n
C1
100u
Q4
C1815
R2
12k
FB
GND
CONN2
CK
SO
SI
I2C/SPI
GND
CONN3
VPPU
VDDU
D
CKP
ICSP
GND
8-14-18-20 pin PIC
U8
VDDU
VPPU
GND
DIL20
D
CKP
VDDU
D
CKP
PGM
GND
28-40 pin PIC
U7
VPPU
VDDU
DIL40
D
CK
PGM
VDDU
JP1
28 pin
GND
GND
SPI
U3
DIL8
A0
SO
WP
VDDU
A1
CK
SI
GND
I2C/UNIO
U6
DIL8
A0
A1
D
VDDU
WP
CK
SO
R27
10k
R26
10k
100n
C9
GND
by Alberto Maccioni
Sheet: /
File: OPROG.sch
Title: Open programmer – main board
Size: A4
Date: 2016-07-08
Rev: 1.8
KiCad E.D.A. kicad 4.0.0-rc1-stable
Id: 1/1